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Integrated circuit design - Wikipedia

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Decibels relative to full scale (dBFS or dB FS) is a unit of measurement for amplitude levels in digital systems, such as pulse-code modulation (PCM), which have a defined maximum peak level.

dBFS - Wikipedia

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Figure 2. Architecture of one front-end channel of the VATA64-HDR16 ASIC. and sampled with the SH signal. The slow shaper is followed by a Peak & Hold device.

A custom front-end ASIC for the readout and timing of 64

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lâ€™m sufficiently disoriented at this point to scarce recall what I expected of the Tesla Model 3. For years we knew the progression was from the top down starting with very premium high margin low volume vehicles and working our way down toward higher volumes and lower prices.

Tesla Model 3 - NextGen Battery - EVTV Motor Verks

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